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09/944656 Jc072 U.S. PRO

08/31/01			
	Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

<p><b>O.I.P.E.</b></p> <p>SCANNED <u>AK</u> <u>2</u> Q.A. <u>EW</u></p>	<p><b>PATENT DATE</b></p>
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APPLICATION NO. 09/944656	CONT/PRIOR	CLASS 340 455	SUBCLASS 252.1	ART UNIT 2635	EXAMINER Smith
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## APPLICANTS

Dennis Gonyea

2683

**TITLE**

Constant impedance in CMOS input and output gain stages for a wireless transceiver

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
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<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Primary Examiner)		<b>ISSUE FEE</b>	
	_____ (Legal Instruments Examiner)		_____ (Date)	
		<b>ISSUE BATCH NUMBER</b>		
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